



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: BRADLEY J. HOWARD

Filed: NOVEMBER 6, 2003

For: PROCESS FOR USING PHOTO-DEFINABLE LAYERS IN THE  
MANUFACTURE OF SEMICONDUCTOR DEVICES AND  
RESULTING STRUCTURES OF SAME

Serial No.: 10/702,238

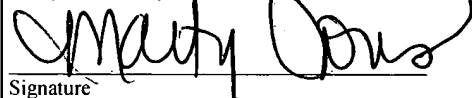
Group Art Unit: 1752

Examiner: LEE, S.

Atty Dkt: 97-0008.02

NUMBER: EV559591584US

I hereby certify that this paper or fee is being deposited with the United States Postal Service "EXPRESS MAIL POST OFFICE TO ADDRESSEE" service, postage prepaid, under 37 CFR 1.10 on the date indicated above and is address to: Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

  
Signature

4/4/06  
Date

**RESPONSE TO FINAL OFFICE ACTION MAILED JANUARY 4, 2006 AND ADVISORY  
ACTION MAILED MARCH 31, 2006**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the final office action mailed January 4, 2006 and advisory action mailed March 31, 2006.

Should any fees under 37 CFR 1.16-1.21 be required for any reason relating to the enclosed materials, including any additional fee for an extension of time, the Commissioner is authorized to deduct such fees from O'Keefe, Egan & Peterman Deposit Account No. 10-1205/MTIP:003D2.

Please amend the application as follows.